



JRW
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Abadeer et al.

Serial No.: 10/605,466

Filed: October 1, 2003

Title: Voltage Divider for Integrated Circuits

Group Art Unit: 2816

Examiner: Terry D. Cunningham

Attorney Docket No.: BUR920030053US1
(02016-00160)

CERTIFICATE OF MAILING (37 C.F.R. 1.8a)

I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, Alexandria, VA 22313-1450.

Lawrence H. Meier *2/28/05*
Lawrence H. Meier Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 28, 2005

TRANSMITTAL OF
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.97(b)

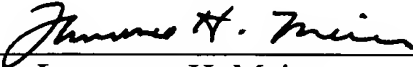
In connection with the above-identified application, please find attached a supplement to the Information Disclosure Statement filed on October 1, 2003, along with a copy of the reference cited therein.

The Information Disclosure Statement transmitted herewith is being filed after the mailing date of a first Office Action on the merits. Therefore, a fee in the amount of \$180 is due in connection with this submission.

The Commissioner is hereby authorized to charge payment of the fees associated with this submission, to Deposit Account No. 09-0456.

Respectfully submitted,

DOWNS RACHLIN MARTIN PLLC

By: 
Lawrence H. Meier
Attorney of Record
Registration No.: 31,446
USPTO Customer No.: 44024
Tel: (802) 863-2375

BTV.430133.1



FORM PTO-1449 (Modified)

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

Page 1 of 1

(Use several sheets if necessary.)

ATTY DOCKET NO.:
BUR920030053US1
(02016-00160)SERIAL NO.:
10/605,466

APPLICANTS: Abadeer et al.

FILING DATE:
10/01/2003GROUP:
2816

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINERS INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						

U.S. PATENT APPLICATION PUBLICATIONS

	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

	AM		<i>A Low Cost Voltage Controlled Amplifier</i> ; Mr. P.V. Wilson, Electronics Australia, Volume 43, No. 1, January, 1981; pp. 1 and 73.					
	AN							
	AO							

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.